Imperas Newsletter: May 2017

"Silicon without software is just sand."
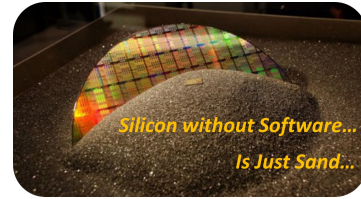
Silicon without Software...
Is Just Sand...

Imperas Software DAC 2017

Imperas will exhibit and deliver a virtual platform tutorial at DAC 2017: Linux Bring Up on Heterogeneous Multiprocessor SoCs with John Min of Imagination Technologies and Simon Davidmann of Imperas Software on Monday June 19. Join us at the World of IoT Pavilion, Booth #521, in Austin @DAC2017!
Read more.

RISC-V Gains a Software Development Solution from Imperas

Imperas announced their membership in the RISC-V Foundation, along with Imperas virtual platform and model support for the RISC-V architecture, available now. Imperas is demonstrating these embedded software development solutions at the 6th RISC-V Workshop in Shanghai, China May 8-11, 2017 and also at the Design Automation Conference (DAC) 2017 in Austin, Texas June 18-22, 2017. Read more. See the presentation here.

Video: Agile for Embedded Software

At the Agile for Embedded Conference in the UK, Imperas presented a technical paper on "Target-Based Simulation for Testing of Embedded Software in CI Flows." Embedded systems have high reliability requirements that demand extensive software testing. Virtual platforms (software simulation) enable the use of unmodified, production binaries, deliver superior controllability, observability and determinism, are easily automated for inclusion in a CI flow, and complement hardware-based testing.
Watch the video here.

Imperas Software Selects eSOL TRINITY for Distribution Partnership in Japan

New Imperas Virtual Platform Software Delivers Performance and Models for Automotive, IoT and Security

New Release Doubles Performance, Adds ARM, Imagination Technologies, RISC-V and Renesas Models, Features Virtual Prototype Modeling Tool

Imperas' newest software release is focused on enhanced solutions across automotive, IoT, security and other markets, and extends Imperas' leadership in virtual prototyping. Highlights include:
• 2x performance improvement in simulation, for fast virtual platform execution to help embedded software and hardware developers run more tests in less time.
• New Open Virtual Platforms (OVP) models, widely-adopted in these markets.
• A new iGen modeling productivity tool, for accelerated peripheral and platform development and customization.
Read more here.

New Open Virtual Platforms Processor Models for ARM, Imagination Technologies, RISC-V and Renesas Accelerate Software Development

Latest Models and Virtual Prototype Software Available Now

Imperas announced the availability of new Open Virtual Platforms (OVP) models for ARM, Imagination Technologies, RISC-V and Renesas processors, along with a new OVPsim software release including the iGen modeling tool. For embedded software and hardware developers, virtual platforms are increasingly important, especially for multi-core designs. These new OVP library models include ARMv8.1, Imagination Technologies MIPS I6400, Renesas RH850, and RISC-V.
Read more.

eSOL TRINITY Partners with Imperas to Deliver Sales and Support for Imperas Virtual Platforms Accelerating Embedded Systems and Software Development, Debug and Test.
[Read more](#).

OVPsim Release News

OVP: Fast Simulation, Free open source models, Public APIs: Open Virtual Platforms.

A new Imperas and OVP release became available, May 2017.
The Open Virtual Platforms portal is one of the most exciting open source software developments in the embedded software world since GNU created GDB.

- For embedded software developers, virtual platforms are increasingly important, especially for multi-core designs.

The resources on this portal can significantly accelerate your development and test. The next release of OVPsim is expected to be available in August 2017.