



Revolutionizing Embedded Software Development

Imperas Newsletter: December 2016

"Silicon without software is just sand."

Updating you on what's new in the embedded software revolution.



Viewpoint: Simon Davidmann, CEO Imperas

Happy holidays, and best wishes for a happy, healthy and prosperous 2017 from all of us at Imperas!

2016 was a banner year for Imperas, and we thank you, innovators all! Our results lead us to believe this year is an inflection point in the adoption of virtual platform based tools.

As we look forward to the new year, our users can expect more: more of the fastest processor models, more tools for software development, more expansion of the Imperas ecosystem via partners.

- More models: ARM, Imagination Technologies / MIPS, Renesas...[contact us about RISC-V](#).
- More tools: For pre-silicon software development, for system-level software development, debug and test, and for security and safety-critical systems: power/ timing estimation, fault simulation, hypervisor-aware tools, Agile / Continuous Integration (CI) support.
- More partners: Expanding the value with OS, tools, methodologies and services relationships.

Imperas News!



Imperas is Hiring!



Imperas is a small but growing company. We are completely employee owned; there is no outside funding, so we are our own bosses. We are looking for simulation and modeling engineers in our Oxford, England headquarters, and looking for field application engineers (FAEs) and technical marketing engineers (TMEs) in the headquarters office and in Silicon Valley and Austin, Texas. In addition to the usual skill set for each position, applicants should have experience in one or more of the following areas: embedded software and firmware, embedded operating systems and drivers, virtual platform simulators, high level SoC modeling. Applicants for FAE and TME positions should have excellent communication skills.

If you would like to be part of our dynamic environment, please go to www.imperas.com/jobs to submit your resume.



Imperas Software Presentation from DVClub Europe

Missed DVClub Europe? See the presentation “Software Verification for Low Power, Safety Critical Systems” by Imperas CEO Simon Davidmann.

How do virtual platforms – software simulation – complement hardware-based testing of software?

- Run full production binaries including hypervisors and hardware virtualization to assist in verification of safety-critical systems
- Virtual platforms become essential with adoption of Continuous Integration methods for embedded systems software

How can virtual platforms for software verification help you?

- Higher quality software
- Reduced development schedules
- Increased software project predictability
- Reduced delivery risk

[View the video here.](#)

[See the presentation slides here.](#)

Imperas Case Study Audi

Learn about software testing applications in the automotive industry.



[Read more here.](#)



Virtual Platform Papers at embedded world 2017

Imperas will attend the [embedded world conference](#), March 14-16, 2017 at the Exhibition Centre in Nürnberg, Germany. Imperas will be featuring two papers:

- "Fast Fault Injection to Evaluate Multicore Systems Soft Error Reliability."
- "Using Virtual Prototypes to Improve the Traceability of Critical Embedded Systems."

To set up a meeting at embedded world, please email us at sales@imperas.com.



Homogeneous And Heterogeneous Computing Collide!

"Processing architectures continue to become more complex, but is the software industry getting left behind? Who will help them utilize the hardware being created?" New article by Brian Bailey, in Semiconductor Engineering, with Larry Lapides. Part one in a series.

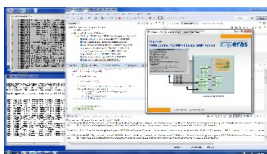
[Read more here.](#)



The Limits Of Parallelism

"How efficient, effective and widespread has parallelism really become? Tools and methodologies have improved, but problems persist." New article by Ann Steffora Mutschler, in Semiconductor Engineering, with Simon Davidmann.

[Read more here.](#)



Imperas Videos

View videos here! All about virtual platforms, applications, and demos of Imperas embedded software development, debug and test solutions.

[View demo videos here.](#)

OVPsim Release News

OVP: Fast Simulation, Free open source models, Public APIs: Open



Virtual Platforms.

A new Imperas and OVP release became available, October 2016.

The [Open Virtual Platforms](#) portal is one of the most exciting open source software developments in the embedded software world since GNU created GDB.

- For embedded software developers, virtual platforms are increasingly important, especially for multi-core designs.

The resources on this portal can significantly accelerate your development and test. The next release of OVPsim is expected to be available in January 2017.



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