



Revolutionizing Embedded Software Development

Imperas Newsletter: November 2017

"Silicon without software is just sand."



Andes and Imperas Partner to Deliver Models and Virtual Platforms for Andes RISC-V Cores

Imperas provides integrated virtual prototype software solutions and models for Andes RISC-V based architecture AndeStar V5 NX25 and N25 processors.

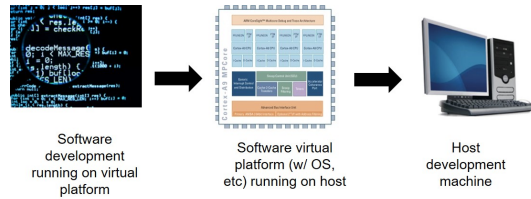
[Read more here.](#)



RISC-V Conference

Imperas will participate and present high-performance software simulation and virtual platforms at the [7th RISC-V Workshop](#). The Imperas session covers virtual prototypes for software development, debug and test. This RISC-V Workshop, hosted by Western Digital in Milpitas California November 28-30 2017, brings the RISC-V community together to share information about RISC-V projects underway around the globe, and build consensus on the evolution of the instruction set.

[Read more here.](#)



Accelerating OS Bring-up And Software Debug across the Spectrum of Electronics Systems

New article in Embedded Systems Engineering: Insights from a virtual platform methodologies case study:

- OS bring-up and testing on an Altera Cyclone V SoC FPGA (with Arm Cortex-A9).

[Read more.](#)



Fast Processor Models of Latest Arm Cores Released by Imperas and Open Virtual Platforms (OVP)

Imperas and OVP announced the availability of models and virtual platforms for the Arm Cortex-A32, Cortex-A35, Cortex-A55, Cortex-A73, Cortex-A75 processors, including ARMv8.1 and ARMv8.2 support.

This extends the OVP processor model library to over 200 models across a spectrum of IP vendors. Over 50 Arm cores are supported, including Cortex- A, Cortex-R and Cortex-M families, to accelerate embedded software development.

[Read more here.](#)

SEMICONDUCTOR ENGINEERING



MCS Event 2018

Imperas will present at the MCS 6th International workshop on the integration of mixed-criticality subsystems on multi-core and many-core processors, January 23, 2018, in Manchester (UK). Duncan Graham will speak on "Virtual platforms for mixed-criticality systems."



Embedded World 2018 and Imperas

See Imperas at the [Embedded World Exhibition & Conference 2018](#): 27 February - 1 March, at the Nuremberg Exhibition Centre, Germany.

Imperas will present their co-authored paper with eSOL: "Virtual Platform Environment for the Bring Up and Test of a Secure Many-Core RTOS (Real Time Operating System)."



ARM TechCon

Imperas exhibited and participated in a lively panel discussion "[Hypervisors: A Real Trend in Embedded, or Just Hype?](#)" at the 2017 ARM TechCon, October 2017.

If you missed the show, you can still see a demonstration of Imperas virtual platforms for

How To Handle Concurrency

New article in Semiconductor Engineering by Brian Bailey on Handling Concurrency, with Simon Davidmann.

System complexity is skyrocketing. The evolution of processing architectures has solved many problems within a chip, but for each problem solved another one was created.

Concurrency is one of those issues, and it has been getting much more attention lately...
[Read more here.](#)

embedded software development, debug and test for ARM-based systems. [Click here to request a demo.](#)
[Read more here.](#)



International System-on-Chip (SoC) Conference

Imperas participated in the [15th International System-on-Chip Conference](#), presenting a paper: "RISC-V Models and Simulation Enable Early Software Bring Up." in October at the University of California, Irvine. Theme: "Secure and Intelligent Silicon Systems for Emerging Applications."
[See the presentation here.](#)

OVPsim Release News

OVP: Fast Simulation, Free open source models, Public APIs: Open Virtual Platforms.



A new Imperas and OVP release became available October 2017. The [Open Virtual Platforms](#) portal is one of the most exciting open source software developments in the embedded software world since GNU created GDB.

- For embedded software developers, virtual platforms are increasingly important, especially for multi-core designs.

The resources on this portal can significantly accelerate your development and test. The next release of OVPsim is expected to be available in December 2017.

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