Imperas Newsletter - December 2019

imperas Revolutionizing Embedded Software Development

What's new from Imperas - December 2019

Preparations are underway as we will be a supporting sponsor to the 2nd annual RISC-V Summit this week in San Jose. We will be presenting two conference papers on the verification challenges of an open ISA and machine learning as well as giving a tutorial based on our collaboration with Google around its instruction stream generator. An approach to machine learning architectural exploration will also be highlighted in a presentation by our CEO Simon Davidmann. Find out more about our participation here

NEWS

We announced the release of the latest update to the RISC-V compliance test suite for RV32I base RISC-V configuration. Developed in conjunction with the RISC-V Foundation's Technical Committee task group for compliance, we have achieved an almost 100% functional coverage of the instructions in the RISC-V ISA base specification known as RV32I. More details here

To support the news by our close partner Andes on the announcement of the new vectors processor NX27V, Charlie Hong-Men Su, CTO and Executive Vice President at Andes Technology Corp. said. "Andes is pleased to certify the Imperas model and simulator as a reference for the new Vector processor NX27V, and is already actively used by our mutual customers." More details here

We also continue to be featured in industry articles, most recently Anna Steffora Mutschler wrote an interesting article (Open ISAs gaining traction) on open-source ISA, followed by Brian Bailey writing on verification with comment from our CEO Simon Davidmann on the role of abstraction (Rapid evolution for verification plans). Both articles were featured in Semiconductor Engineering in September and October.

A new Imperas and OVP release became available in November 2019, reference 20191106.0

The Open Virtual Platform portal is one of the most exciting open-source software developments in the embedded software world since GNU created GDB.

For embedded software developers, virtual platforms are increasingly important, especially for multi-core designs.

EVENTS

The last event of 2019 will be the RISC-V Summit in San Jose in December. Please visit our website for more information or contact us directly to set up a meeting.

RISC-V Summit 2019 (December 10-12)

Learn More about Imperas products, tools and solutions for Virtual Platforms!

Please email info@imperas.com to set up a meeting with Imperas at this event, or for more information about virtual platforms for embedded software and systems development, debug and test. We hope to see you at an upcoming event!

Book a Meeting
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