Imperas Newsletter: April 2018

"Silicon without software is just sand."

EVENTS

See Imperas at RISC-V Workshop Barcelona

Imperas will participate in the RISC-V Workshop Barcelona in May 2018 in Spain, and invites you to "Join the RISC-V Revolution!" and be part of the disruptive force transforming the microprocessor IP market through open standard collaboration. The RISC-V Workshop Barcelona 2018 will feature an Imperas exhibit, and two presentations on virtual platform technology for RISC-V based designs:

- "A Common Software Development Environment for Many-core RISC-V based Hardware and Virtual Platforms"
- "RISC-V Virtual Platforms for Early RISC-V Embedded Software Development"
- See a live demo of SMP Linux booting on a RISC-V virtual platform including the SiFive U54-MC, and catch up on all the latest Imperas developments

Where: Universitat Poletècnica de Catalunya Barcelona Tech, in Barcelona, Spain.
When: May 7 - 10, 2018.
Read more here.

IoT /M2M Expo Japan

The Spring IoT/M2M Expo is coming up in May, at Tokyo Big Sight. Imperas' distributor, eSOL TRINITY, will be exhibiting, and will be available to discuss Imperas virtual platform solutions at the event.
Where: Tokyo International Exhibition Center (Tokyo Big Sight), Japan.
When: May 9 - 11, 2018.

Read more here.

DATE 2018

[DATE (Design, Automation & Test in Europe) 2018](#), Dresden, Germany, in March, featured two presentations based on Imperas virtual platform technology:

- "[Early Evaluation of Multicore Systems Soft Error Reliability Using Virtual Platforms](#)" authored by Felipe Rocha da Rosa of Universidad Federal Rio Grande Sud.
- "[Virtual Prototype Makani: Analyzing the Usage of Power Management Techniques and Extra-functional Properties by Using Virtual Prototyping](#)" by Sören Schreiner, OFFIS – Institute for Information Technology.

[Read more here.](#)

Automotive Testing Expo in Korea

Imperas distribution partner Coontec presented Imperas virtual platform solutions for automotive software debug, test and verification, including fault simulation and RISC-V platform demos, at the Automotive Testing Expo in Seoul, Korea, March, 2018.
[Read more here.](#)

Embedded World 2018

At the February [Embedded World Exhibition & Conference 2018](#) in Germany, Imperas demonstrated virtual platforms solutions in the [RISC-V Foundation](#) booth, featuring technology to accelerate embedded software development and test. Imperas also presented two co-authored papers.
[Read a summary of the action @ Embedded World here!](#)

VIDEOS

[SiFive RISC-V U54-MC Virtual Platform Booting SMP Linux with Imperas Multi-Core Debugger](#)

[Imperas at Embedded World 2018](#)

[See more Imperas videos here!](#)

OVPsim Release News

OVP: Fast Simulation, Free open source models, Public APIs: Open Virtual Platforms.

A new Imperas and OVP release became available March 2018.
The [Open Virtual Platforms](#) portal is one of the most exciting open source software developments in the embedded software world since GNU created GDB.

- For embedded software developers, virtual platforms are increasingly important, especially for multi-core designs.

The resources on this portal can significantly accelerate your development and test. The next release of OVPsim is expected to be available in May 2018.