

Revolutionizing Embedded Software Development

Imperas Newsletter: December 2015

"Silicon without software is just sand."

Updating you on what's new in the embedded software revolution.





Viewpoint: Simon Davidmann, CEO Imperas

As we finish up 2015, the Imperas team would like to thank you for your support. It has been a banner year for Imperas, with significant achievements in processor models, software tools, partnerships and market adoption of our solution for embedded software development.

As we look forward to 2016, our users can expect more of the same: more of the fastest processor models, more tools for software development, debug and test, more expansion of the Imperas ecosystem by working with our partners.

Have a great holiday season, and best wishes for a happy, healthy and prosperous 2016.

Please follow us: on Linked In, and twitter @ImperasSoftware

Imperas Demonstrated Virtual Platforms at ARM TechCon 2015



Imperas Virtual Platforms for Software Development and Test.

At ARM Techcon, Imperas demonstrated virtual platform based embedded software debug, analysis and verification, featuring:

- The fastest ARM models
- Demonstrations of a 32-quad core system simulation based on ARM Cortex-A72 booting Linux: 128 processors in all
- QuantumLeap parallel simulation acceleration, which increases Imperas' industry leading virtual platform performance advantage
- CPU- and OS-aware Verification, Analysis and Profiling (VAP) tools

Read more here.

Imperas and Seltech Collaborate on Hypervisor Development and Deployment



Imperas Partners with Seltech, Provider of Hypervisor and Other Embedded Software Solutions.

Imperas announced a collaboration with Seltech Corporation for the development and deployment of the Seltech FEXER OX hypervisor. FEXER OX has been ported to MIPS-based virtual platforms, with other processor platforms to follow. Imperas and Seltech are also working together on tools in the Imperas M*SDK product for the bring up of guest operating systems, bare metal applications and testing of security in the hypervisor-based software stack.

Read more here.

eSOL TRINITY Signed by Imperas as Technical Partner and Distributor for Japan



Imperas Software and eSOL TRINITY partner for sales and technical support of Imperas virtual prototyping solutions for Japan.

Imperas and eSOL TRINITY Co., Ltd. (TRINITY), a premier solutions provider for the design and development of embedded software, have agreed TRINITY becomes a new technical partner and distributor for Japan. TRINITY will provide pre- and post-sales technical support, consulting, training, delivery and implementation for the complete portfolio of Imperas virtual prototyping solutions. TRINITY is a wholly owned subsidiary of eSOL Co., Ltd., the leading provider of real-time embedded software solutions.

Read more here.

Imperas: from Today to Tomorrow, with Intent

Peggy Aycinena Interview on EDA Café.

Simon Davidmann discusses software complexity and how to transcend hardware / software disconnects.

Read more here.

Software and Memory Footprint Challenge Traditional EDA

John Blyler talks to Simon Davidmann about Embedded Software.

"Software running close to the silicon hardware still presents many challenges for designs in terms of memory footprint optimization and security vulnerability...."



Read more here.

Imperas Simon Davidmann on Upcoming Hurdles For The Semiconductor Industry

Ann Steffora Mutschler of Semiconductor Engineering rounds up Experts at the Table.

Part 1 of "Upcoming Hurdles For The Semiconductor Industry" with experts at the table: From business models to the death throes of Moore's Law; and the software challenge to the evolving value chain.

Read more here.

OVPsim Release News

OVP: Fast Simulation, Free open source models, Public APIs: Open Virtual Platforms.



The Open Virtual Platforms™ (OVP™) portal is one of the most exciting open source software developments in the embedded software world since GNU created GDB.

- For embedded software developers, virtual platforms will be increasingly important, especially for multi-core designs. The resources on this portal can significantly accelerate your development and test.
- Explore what is new at <u>OVPworld!</u>

The current OVPsim release is 20151203.0 (December 2015)

- Please check the <u>release notes on OVPworld.org</u> for more details.
- The next release of OVPsim is expected to be available in March 2016.



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